

09/800224

Cote

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Branko D. KOVACEVIC
Assignee: ATI Technologies, Inc.
Title: SYSTEM FOR DIGITIZED AUDIO STREAM SYNCHRONIZATION AND METHOD THEREOF
Patent No.: 7,030,930 B2 Issued: April 18, 2006
Atty. Docket No.: 1376-0100450

MS: Certificate of Correction Branch
COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, VA 22313-1450

**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT—
PTO MISTAKE (37 C.F.R. § 1.322(a))**

Dear Sir:

Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322(a), please issue a Certificate of Correction in the above-identified matter. The mistake(s) to be corrected was made by the Office.

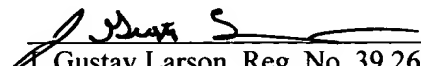
1. Attached hereto, in duplicate, is Form PTO-1050, with at least one copy suitable for printing.
2. The exact page(s) and line number(s) where the error(s) is shown correctly in the application file:
Response to Final Office Action dated October 24, 2005, pages 3 and 4
3. Please send the Certificate to:

J. GUSTAV LARSON
LARSON NEWMAN ABEL POLANSKY & WHITE, LLP
5914 WEST COURTYARD DRIVE, SUITE 200
AUSTIN, TEXAS 78730

**Certificate
JUL 07 2006
of Correction**

Respectfully submitted,

6-26-06
Date


J. Gustav Larson, Reg. No. 39,263
Attorney for Applicant(s)
Larson Newman Abel Polansky & White, LLP
5914 West Courtyard Drive, Suite 200
Austin, Texas 78730
512-439-7100 (phone) 512-439-1799 (fax)

JUL 07 2006

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO : 7,030,930 B2

DATED : April 18, 2006

INVENTOR : Branko D. KOVACEVIC

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column No. 13, Line No. 8 change "die" to --the--

Column No. 13, Line No. 32 change "wit" to --with--

MAILING ADDRESS OF SENDER:

Larson Newman Abel Polansky & White, LLP
5914 West Courtyard Drive, Suite 200
Austin, TX 78730

PATENT NO. 7,030,930 B2

No. of additional copies



This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

APR 18 2006

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO : 7,030,930 B2

DATED : April 18, 2006

INVENTOR : Branko D. KOVACEVIC

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If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Branko D. KOVACEVIC

Title: SYSTEM FOR DIGITIZED AUDIO STREAM SYNCHRONIZATION
AND METHOD THEREOF

App. No.: 09/800,224

Filed: 03/06/2001

Examiner: YENKE, Brian P.

Group Art Unit: 2614

Customer No.: 34456

Confirmation No.: 3321

Atty. Dkt. No.: ATI.0100450
(1376-0100450)

Mail Stop AF
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

In response to the Final Office Action mailed August 25, 2005, the Applicant respectfully requests favorable reconsideration of the above-identified patent application in view of the following amendments and remarks, which are believed to place the above-identified patent application in condition for allowance or in better form for consideration on appeal.

Claim Amendments begin on page 2.

Remarks begin on page 7.

CERTIFICATE OF TRANSMISSION/MAILING	
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to the Commissioner for Patents on <u>10/24/05</u> .	
<u>Judy Carey</u> Typed or Printed Name	<u>[Signature]</u> Signature

07 2005

Claim Amendments:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Cancelled)

2. (Cancelled)

3. (Cancelled)

4. (Cancelled)

5. (Cancelled)

6. (Cancelled)

7. (Cancelled)

8. (Cancelled)

9. (Cancelled)

10. (Cancelled)

11. (Cancelled)

12. (Cancelled)

13. (Cancelled)

14. (Cancelled)

15. (Cancelled)

16. (Previously Presented) A system comprising:

- a data processor having an I/O buffer;
- a memory having an I/O buffer coupled to the I/O buffer of the data processor, the memory storing code to control said data processor to:
 - determine a delay amount, wherein the delay is associated with a difference between an amount of time to process a portion of video data to amount of time to process a portion of audio data associated with the video data;
- assert a transfer of processed audio data to memory through a data port;
- assert, after waiting the delay amount, a transfer of a representation of the processed audio data from memory to an audio output through a data port;
- a video processor to process the portion of video data to generate processed video data;
- an audio processor to process the portion of audio data to generate processed audio data;
- a memory controller comprising:
 - a first register portion to enable a transfer of the processed audio data to memory;
 - and
 - a second register portion to enable a transfer of the representation of the processed audio data from memory to an audio output.

17. (Original) The system as in Claim 16, wherein said video processor further includes:

- an analog video encoder to:
 - digitize analog video data to generate digital video data;
 - decode digital video data to analog video;
- provide digital video data related to interlaced video data to a digital video processor;
- provide the video data to the video output; and
- said digital video processor to de-interlace digital video data related to interlaced video data.

18. (Original) The system as in Claim 17, wherein the digital video processor is to further process MPEG transport packets into video data.

19. (Original) The system as in Claim 16, wherein the audio processor is to further digitize analog audio data to digital audio data.

20. (Original) The system as in Claim 16, wherein the data port is coupled to the video processor.

21. (Original) The system as in Claim 16, wherein the data port is coupled to the video processor to transfer the processed audio data to and from memory.

22. (Original) The system as in Claim 16, further including a host bus interface unit, with an I/O bus coupled to the I/O bus of the data processor, to provide access to memory to the data port.

23. (Original) The system as in Claim 16, wherein the delay is determined according to a mode of operation.

24. (Original) The system as in Claim 23, wherein the mode of operation includes processing multimedia data associated with analog audio and video content to generate the portion of audio data and the portion of video data.

25. (Original) The system as in Claim 23, wherein the mode of operation includes processing multimedia data associated with digital audio and video content to generate the portion of audio data and the portion of video data.

26. (Original) The system as in Claim 23, wherein the mode of operation includes generating picture-in-picture video data by mixing multimedia data associated with digital audio and video content with video data associated with an analog video content.

27. (Original) The system as in Claim 23, wherein the mode of operation includes generating picture-in-picture video data by mixing multimedia data associated with analog audio and video content with video data associated with an analog video content.

28. (Original) The system as in Claim 16, wherein memory is to further store code to:
assign a first address associated with a first portion of memory to store processed audio data;
assigning a second address associated with a second portion of memory to read a set of a representation of the processed audio data.

29. (Original) The system as in Claim 28, wherein a rate of transfer of processed audio data is altered to keep an amount of stored data between the first and second addresses constant.

30. (Original) The system as in Claim 16, wherein the data port is a multiple channel data access port and further includes:
a first FIFO channel for transferring the processed audio data to memory; and
a second FIFO channel for transferring the representation of the processed audio data from memory.

31. (Original) The system as in Claim 30, further including a third FIFO channel for performing general functions.

32. (Original) The system as in Claim 31, wherein the first and second port include separate controls for initiating data transfers.

33. (Cancelled)

34. (Cancelled)

35. (Cancelled)

36. (Cancelled)

4006

REMARKS

The Office Action dated August 25, 2005, has been received and carefully considered. In this Response, claims 1-15 and 33-36 have been cancelled without prejudice.

Allowability of Claims 16-32

The Applicant notes with appreciation the indication at page 6 of the Office Action that claims 16-32 are allowed.

Rejections of Claims 1-15 and 33-36

At page 3 of the Office Action, claims 1-2, 4-7, and 15 were rejected under 35 U.S.C. Section 102(b) as being anticipated by Cooper (U.S. Patent No. 5,202,761). At page 3 of the Office Action, claims 3 and 8-14 were rejected under 35 U.S.C. Section 103(a) as being unpatentable over Cooper. At page 5 of the Office Action, claims 33-36 were rejected under 35 U.S.C. Section 103(a) as being unpatentable over Cooper and further in view of Cooper (U.S. Patent No. 6,836,295).

Regardless of whether the claims are obvious or anticipated as alleged, in the interest of moving the present Application forward, Claims 1-15 and 33-36 have been cancelled without prejudice. Accordingly, reconsideration and withdrawal of the rejections is respectfully requested.

Conclusion

The Applicants respectfully submit that the present application is in condition for allowance, and an early indication of the same is courteously solicited. The Examiner is respectfully requested to contact the undersigned by telephone at the below listed telephone number in order to expedite resolution of any issues and to expedite passage of the present application to issue, if any comments, questions, or suggestions arise in connection with the present application.

The Applicants believe no additional fees are due, but if the Commissioner believes additional fees are due, the Commissioner is hereby authorized to charge any fees, which may be required, or credit any overpayment, to Deposit Account Number 50-0441.

JUL 07 2006
2006

Respectfully submitted,

Date

10/24/05



Adam D. Sheehan; Reg. No. 42,146
TOLER, LARSON & ABEL, L.L.P.
5000 Plaza On The Lake, Suite 265
Austin, Texas 78746
(512) 327-5515 (phone)
(512) 327-5452 (fax)

JUL 07 2006